



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of

Shunpei YAMAZAKI et al.

Serial No. 09/334,646

Filed: June 17, 1999

For: SEMICONDUCTOR DEIVE AND

FABRICATION METHOD THEREOF

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) Group Art Unit: 2811

) Examiner: S. Hu

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**AMENDMENT AND RESPONSE TO ELECTION REQUIREMENT**

Honorable Commissioner for Patents

Washington, D.C. 20231

Sir:

In response to the Office Action of July 13, 2001, please amend the subject application as follows:

**IN THE CLAIMS:**


Cancel claims 82-99 without prejudice to applicants' right to file a divisional application with respect thereto.

**REMARKS**

In response to the election requirement in the Office Action of July 13, 2001, Applicants hereby elect without traverse the Species I claims - that is, claims 1-3, 8, 11-14, 16-19, 32-34, 38-43, 52-53, 58-60, 65, 71-73, and 75-81, in which the basic switch element is formed of parallel TFT transistors. Please note that Applicants have also elected claim 65 which is believed to be inadvertently omitted from Species I.

Examination on the merits is requested.

Respectfully submitted,

  
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